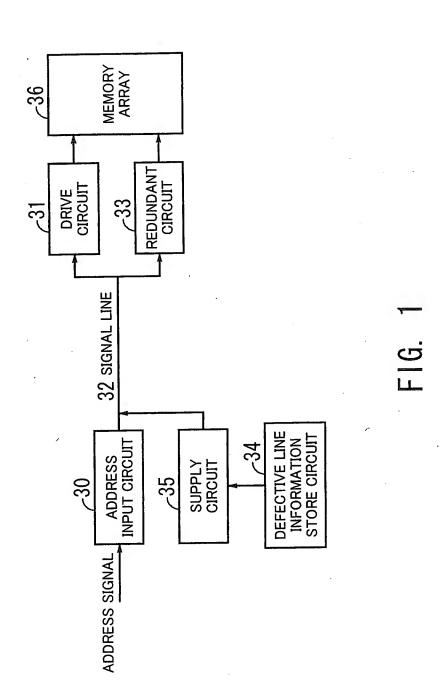
DEVICE
Inventor's Name: YAGISHITA, et al.
Divisional Application of No.: 09/988,614
Docket No.: 107337-00053

Sheet 1 of 14

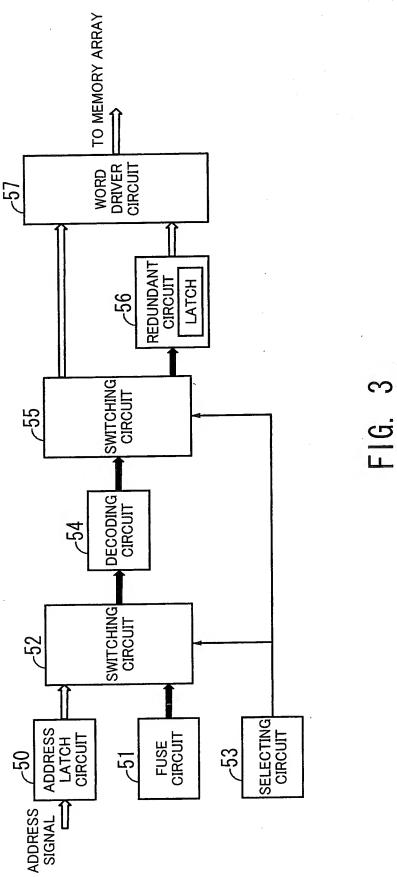


Intle. SEMICONDUCTOR MEMORY
DEVICE
Inventor's Name: YAGISHITA, et al.
Divisional Application of No.: 09/988,614
Docket No.: 107337-00053 Sheet 2 of 14 TO MEMORY ARRAY WORD DRIVER CIRCUIT REDUNDANT CIRCUIT LATCH SWITCHING 55 DECODING CIRCUIT -54 SWITCHING CIRCUIT -52 SELECTING CIRCUIT ADDRESS LATCH CIRCUIT FUSE CIRCUIT -53 5

> ADDRESS SIGNAL

DEVICE
Inventor's Name: YAGISHITA, et al.
Divisional Application of No.: 09/988,614
Docket No.: 107337-00053





ITHE. SEMICONDUCTOR MEMORY

DEVICE
Inventor's Name: YAGISHITA, et al.
Divisional Application of No.: 09/988,614

Docket No.: 107337-00053 Sheet 4 of 14 TO MEMORY ARRAY WORD DRIVER CIRCUIT -57 REDUNDANT CIRCUIT LATCH SWITCHING 55 DECODING CIRCUIT SWITCHING CIRCUIT

SELECTING CIRCUIT

-53

-52

-50

ADDRESS LATCH CIRCUIT

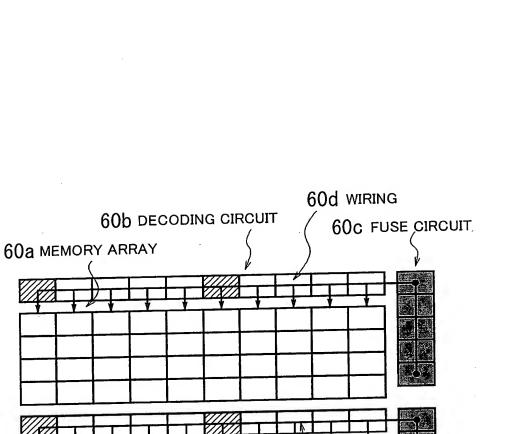
ADDRESS SIGNAL

FUSE CIRCUIT

51

Title: SEMICONDUCTOR MEMORY **DEVICE** Inventor's Name: YAGISHITA, et al. Divisional Application of No.: 09/988,614

Docket No.: 107337-00053



Sheet 5 of 14

61c FUSE CIRCUIT ARRAY > 61b DECODING CIRCUIT 61a MEMORY ARRAY 61d WIRING

FIG. 5

Title. SEMICONDUCTOR MEMORY

DEVICE
Inventor's Name: YAGISHITA, et al.
Divisional Application of No.: 09/988,614
Docket No.: 107337-00053

Sheet 6 of 14

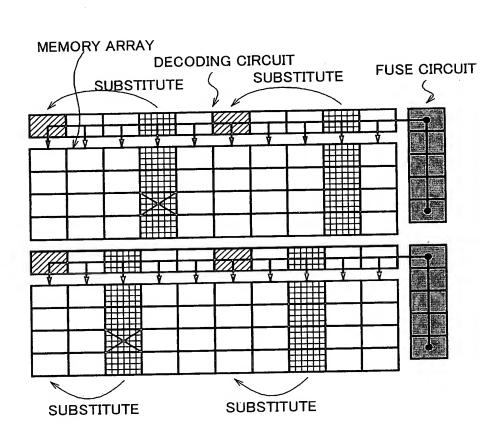
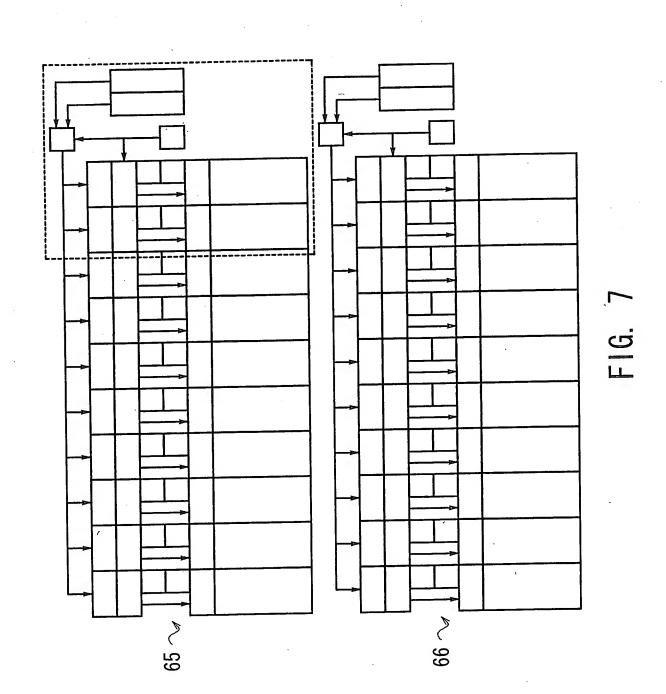


FIG. 6

DEVICE
Inventor's Name: YAGISHITA, et al.
Divisional Application of No.: 09/988,614
Docket No.: 107337-00053

Sheet 7 of 14



Inte: SEMICONDUCTOR MEMORY

DEVICE

Inventor's Name: YAGISHITA, et al.

Divisional Application of No.: 09/988,614

Docket No.: 107337-00053

SWITCHING

74a

75a

DECODING CIRCUIT

SWITCHING CIRCUIT

75b~

75c-

75d-

75e√

75f~

Sheet 8 of 14

ADDRESS LATCH CIRCUIT FUSE CIRCUIT SELECTING CIRCUIT -74e 74b REDUNDANT CIRCUIT LATCH CIRCUIT WORD DRIVER CIRCUIT MEMORY CELL SWITCHING CIRCUIT DECODING CIRCUIT REDUNDANT 74d-74c-WORD DRIVER CIRCUIT LATCH CIRCUIT MEMORY CELL Title: SEMICONDUCTOR MEMORY **DEVICE**

Inventor's Name: YAGISHITA, et al.
Divisional Application of No.: 09/988,614
Docket No.: 107337-00053

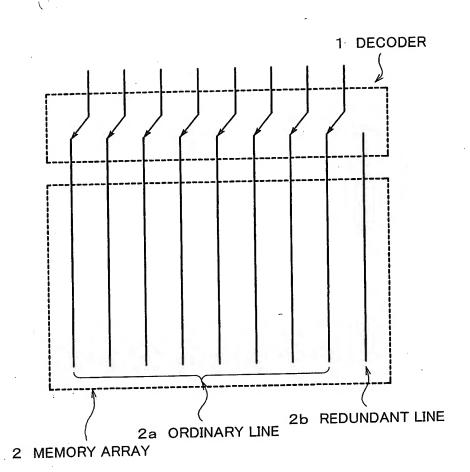


FIG. 9 PRIOR ART

Title: SEMICONDUCTOR MEMORY **DEVICE**

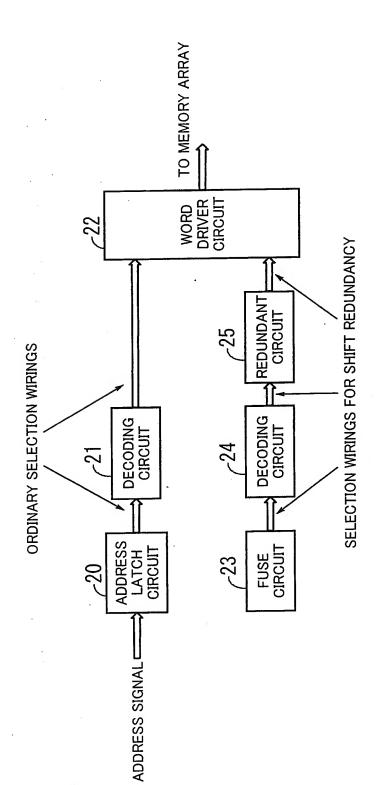
Inventor's Name: YAGISHITA, et al.
Divisional Application of No.: 09/988,614
Docket No.: 107337-00053

Sheet 10 of 14

DEFECTIVE LINE 1 DECODER 2b REDUNDANT LINE 2a ORDINARY LINE 2 MEMORY ARRAY

FIG. 10 PRIOR ART

DEVICE
Inventor's Name: YAGISHITA, et al.
Divisional Application of No.: 09/988,614
Docket No.: 107337-00053
Sheet 11 of 14



Title: SEMICONDUCTOR MEMORY **DEVICE**

Inventor's Name: YAGISHITA, et al. Divisional Application of No.: 09/988,614

Docket No.: 107337-00053

Sheet 12 of 14

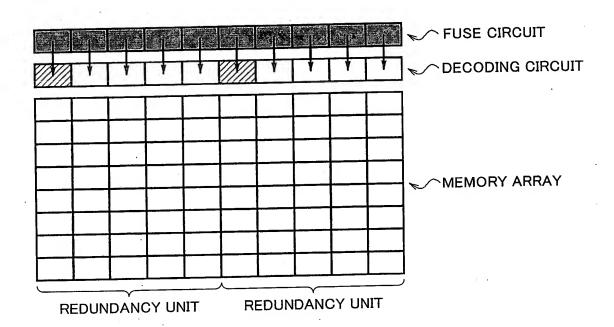


FIG. 12 PRIOR ART

THE: SEMICONDUCTOR MEMORY DEVICE

Inventor's Name: YAGISHITA, et al.
Divisional Application of No.: 09/988,614
Docket No.: 107337-00053

Sheet 13 of 14

FUSE CIRCUIT DECODING CIRCUIT MEMORY ARRAY

DECODING CIRCUIT

MEMORY ARRAY

FIG. 13 PRIOR ART

Title. SEMICONDUCTOR MEMORY DEVICE

Inventor's Name: YAGISHITA, et al. Divisional Application of No.: 09/988,614 Docket No.: 107337-00053

Sheet 14 of 14

SUBSTITUTE SUBSTITUTE FUSE CIRCUIT DECODING CIRCUIT MEMORY ARRAY DECODING CIRCUIT MEMORY ARRAY

FIG. 14 PRIOR ART

DEFECTIVE LINE

DEFECTIVE LINE